



RESEARCH INSTITUTE FOR
SECURE HARDWARE &
EMBEDDED SYSTEMS

RISE First Annual Conference
Press Room, Nova South, London
Wednesday 14th November 2018

| Time | Topic |
|---------------|--|
| 09:30 – 10:00 | Registration – Tea/Coffee |
| 10:00 - 10:10 | Welcome <i>Prof. Máire O'Neill, Director, RISE, Queen's University Belfast</i> |
| 10:10 – 11:00 | Keynote: <i>Jo Van Bulck, KU Leuven</i> Leaky processors and the RISE of hardware-based trusted computing |
| 11:00 – 11:45 | <i>Theo Marketos, University of Cambridge</i> IOSEC: Protection and Memory Safety for Input/Output Security <i>Dan Page, University of Bristol</i> SCARV: A Side-Channel Hardened RISC-V Platform <i>David Oswald, University of Birmingham</i> User-Controlled Hardware Security Anchors: Evaluation And Designs <i>Máire O'Neill, Queen's University Belfast</i> Deep Security: Applying Deep Learning To Hardware Security |
| 11:45 – 12:05 | Introduction to Research New Projects <i>Dirk Koch, University of Manchester</i> rFAS - reconfigurable FPGA Accelerator Sandboxing <i>Simon Moore, University of Cambridge</i> SafeBet Memory capabilities to enable safe, aggressive speculation in processors <i>Pramod Bhatotia, University of Edinburgh</i> GUPT: A Hardware-Assisted Secure and Private Data Analytics Service |
| 12:05 – 12:50 | Lightning Talks – Early Career Researchers <i>Sujoy Sinha Roy, University of Birmingham</i> - Hardware implementation of post-quantum PKC and homomorphic encryption <i>Rishad Shafik, Newcastle University</i> - Secure Power-Compute Co-design <i>Franck Courbon, University of Cambridge</i> - Partial hardware reverse engineering for combined attacks and authenticity verification <i>Vasileios Mavroudis, University College London</i> - Cryptographic Hardware from Untrusted Components <i>Jorden Whitefield, University of Surrey</i> - Formal Analysis and Applications of Direct Anonymous Attestation <i>Elif Kavun, Infineon</i> - TBC <i>Ayesha Khalid, Queens University Belfast</i> - TBC |
| 12:50 – 14:10 | Lunch/ Poster Session/Networking |
| 14:10 – 15:00 | Keynote: <i>Patrick Koeberl, Principal Engineer, Security and Privacy Research, Intel Labs</i> Vehicle to Cloud: Security Research Challenges for Intelligent Vehicles |

| | |
|----------------------|--|
| | |
| 15:00 – 15:45 | Industry Panel Session: Future Research & Innovation Challenges in Hardware Security Chair: Charles Brookson Panel <ul style="list-style-type: none">• Ilhan Gurel, Expert, HW and SW Security, <i>Ericsson</i>• Alex van Someren, <i>Amadeus</i>• Madeline Cheah , Cyber Sec Innovation Lead, <i>Horiba-Mira</i>• Bob Edge, Technical Director, <i>Cyber 1st</i> |
| 15:45 | Close |